Customer No.: 31561 Docket No.: 11612-US-PA

Application No.: 10/709,589

## **AMENDMENT**

## To the Title:

Please amend the title of invention as indicated hereinafter. Specifically, the brackets ("[", "]") at the beginning and the end of the title are removed as per the Examiner's instruction.

"[COLOR CORRECTION CIRCUIT OF DISPLAY AND CORRECTION

METHOD THEREOF]—COLOR CORRECTION CIRCUIT OF DISPLAY AND

CORRECTION METHOD THEREOF"

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To the Specification:

Please amend the as-filed paragraph [0011] as follows.

According to an embodiment of the present invention, the color correction [0011]

circuit is coupled to a video source and a display panel. The color correction circuit

comprises a video look-up circuit, N+M bit data driving circuit and N+M bit data

gamma voltage generating circuit. The video look-up circuit inside the color correction

circuit modulates N bit video data from the video source into N+M bit video data

according to a color look-up table. The modulated video data is transmitted to the

N+M bit data gamma voltage generating circuit through the N+M bit data driving

circuit. The N+M bit data gamma voltage generating circuit provides the voltages in

every step based on the color of the N+M bit video data and the values in the gamma

color correction table that corresponds to the N+M bit video data.

Please amend the as-filed paragraph [0017] as follows.

The present invention is also directed to a method of correcting the colors of [0017]

The method includes the following steps. After receiving N bit video data, a display.

the N bit video data is modulated into N+M bit video data according to a color look-up

Thereafter, the voltage in every step is provided based on the color of the N+Mtable.

bit video data and the values in a gamma color correction table that corresponds to the

N+M bit video data. Finally, the voltages are used to drive the display panel.

Please amend the as-filed paragraph [0028] as follows.

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[0028] Fig. 2 is a flow diagram showing the steps for correcting the colors in a

display according to one embodiment of the present invention. As shown in Figs. 1 and

2, the video look-up circuit 110 picks up N bit video data of the color red, green and

blue from the video source (in step s202). According to a built-in color look-up table,

the N bit video data of the colors are modulated into N+M bit video data and then output

to the N+M bit data driving circuit 120 (in step s204). On receiving the modulated

N+M bit video data, the N+M bit data driving circuit 120 transmit the video data to the

N+M bit data gamma voltage generating circuit 130. Thereafter, the N+M bit data

gamma voltage generating circuit 130 provides the voltages at every step according to

the color of the N+M bit video data and the values in a gamma color correction table

that correspond to the N+M bit video data (in step s206). The N+M bit data gamma

voltage generating circuit 130 can be a gamma correction circuit. However, the scope

of the present invention is not limited as such.

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